

FIGURE 1



22-141 50 SHEETS
 22-142 100 SHEETS
 22-144 200 SHEETS

INTEGRATED CIRCUIT
204

CONFORMAL EMI
SHIELD 100

PRINTED WIRING BOARD
202

FIGURE 2A



22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS

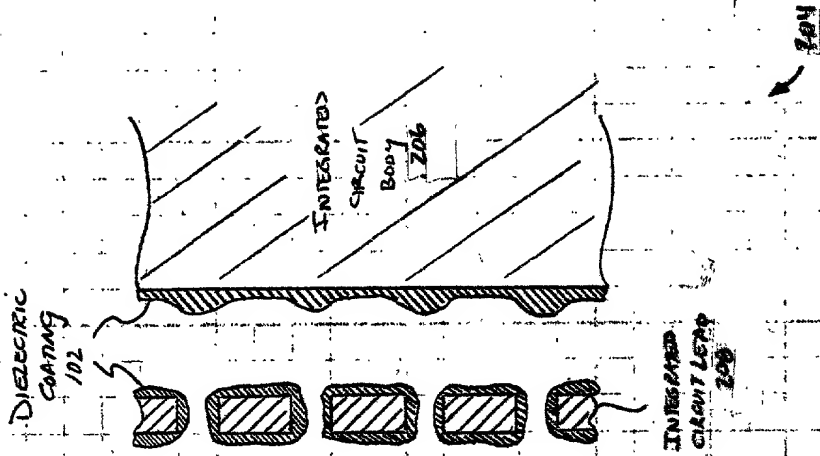
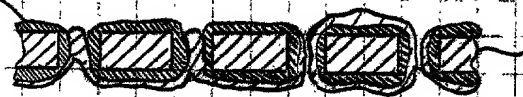
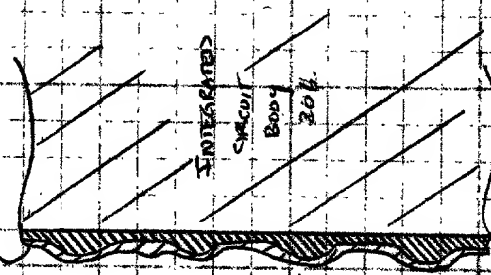


FIGURE 2B

DIELECTRIC
COATING
102



CONDUCTIVE
COATING
104



802
CRYSTALLINE
SEMICONDUCTOR

FIGURE 2E

204



22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS

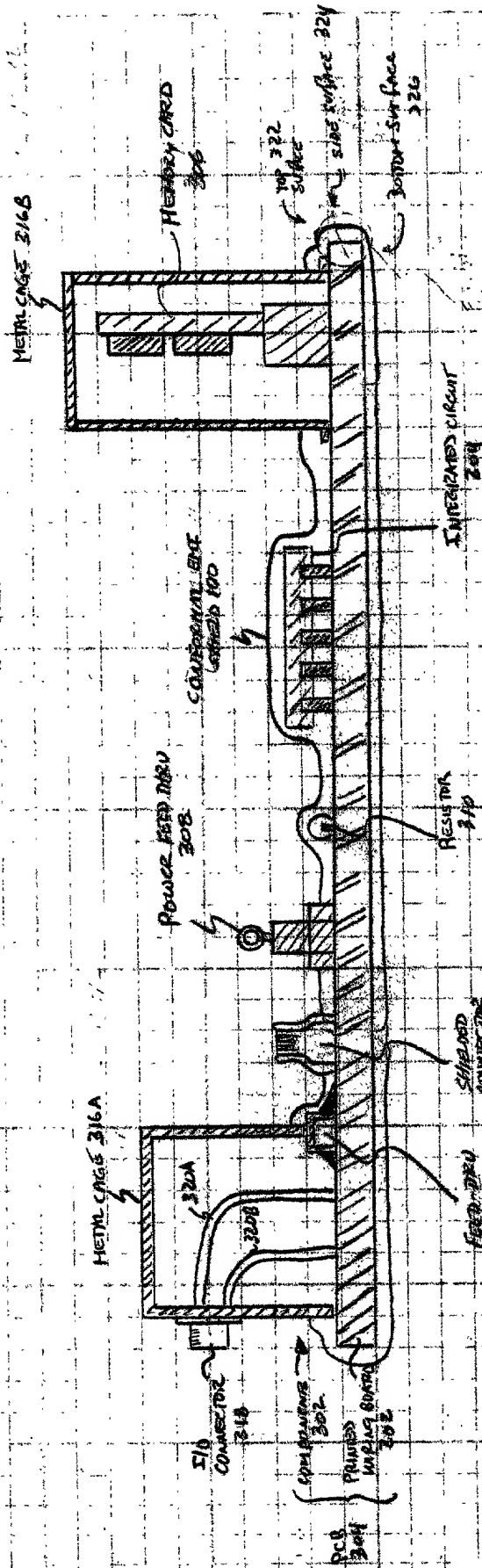


FIGURE 3



22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS

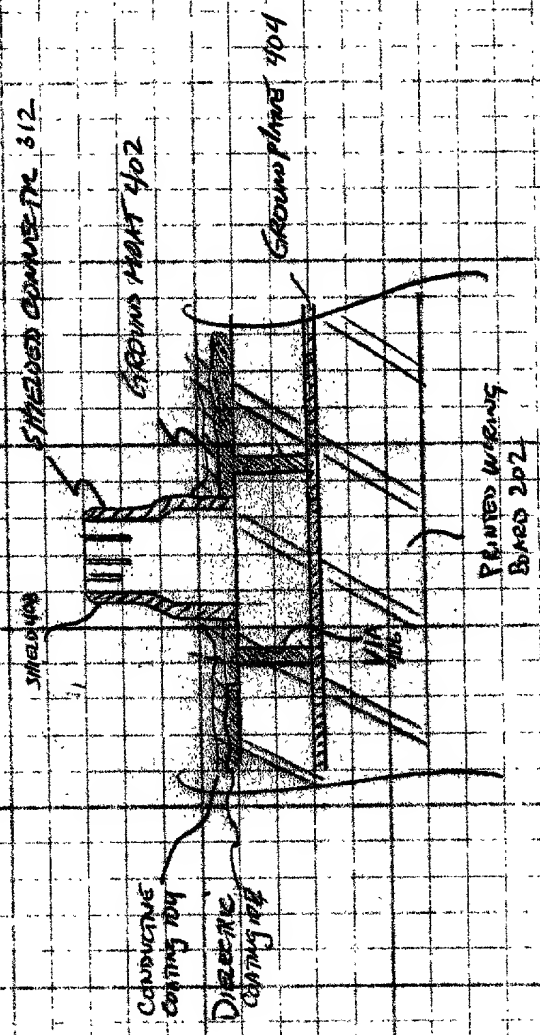


FIGURE 4

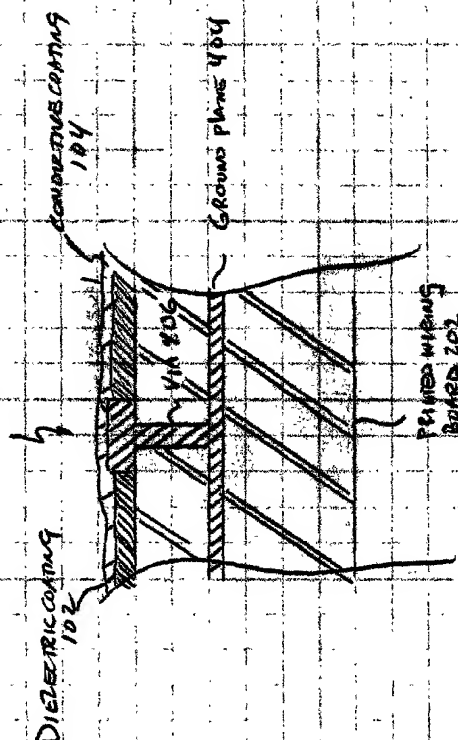


FIGURE 5

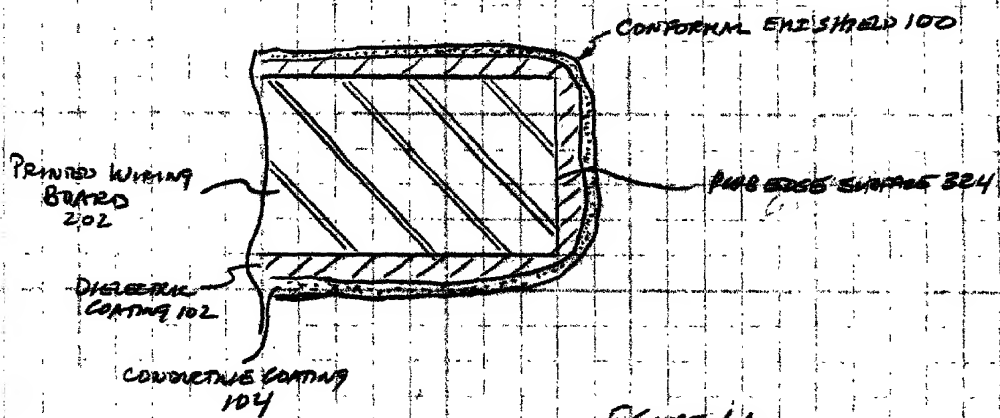


FIGURE 6A

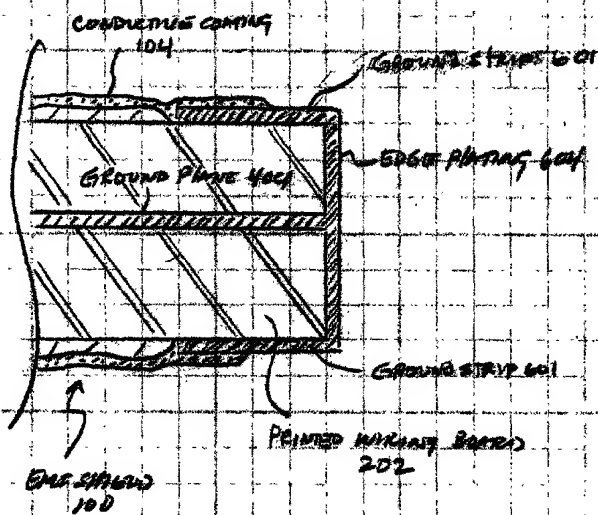


FIGURE 6B

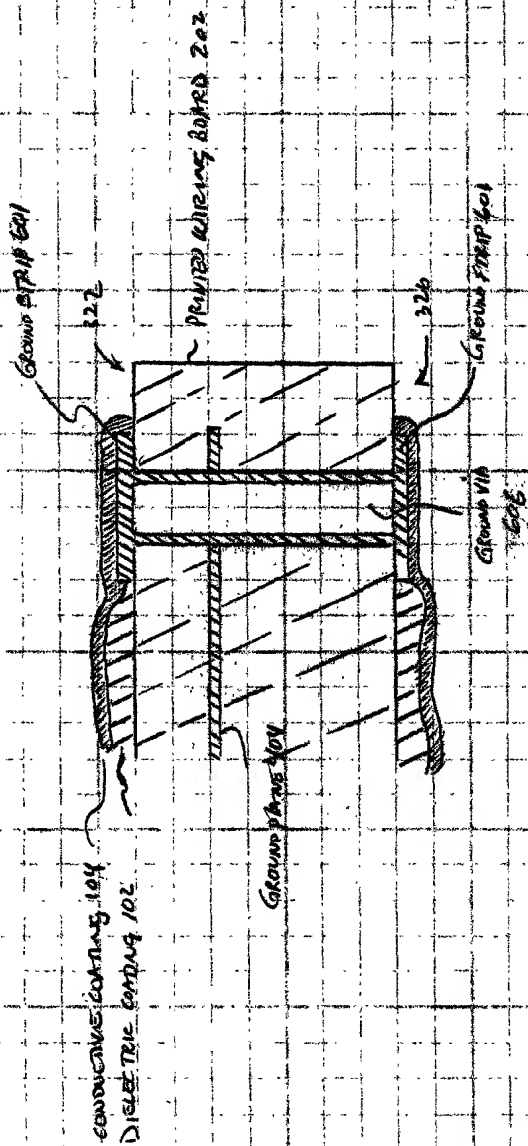


FIGURE 6C

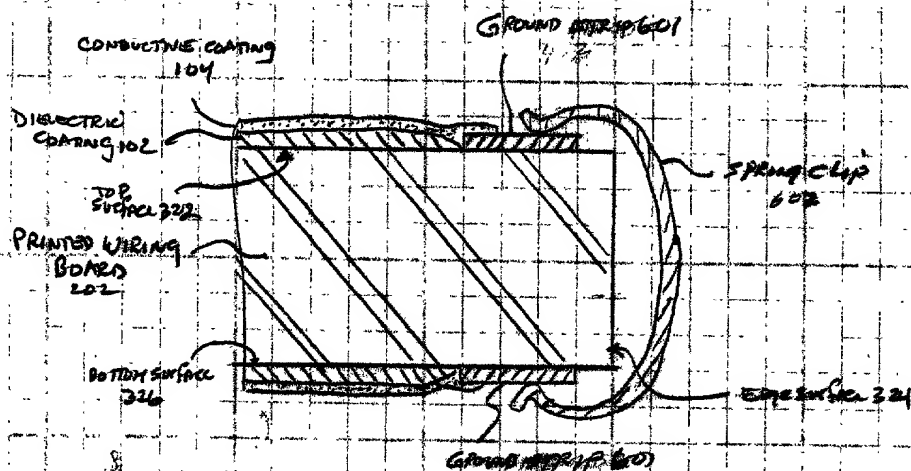


FIGURE 6D

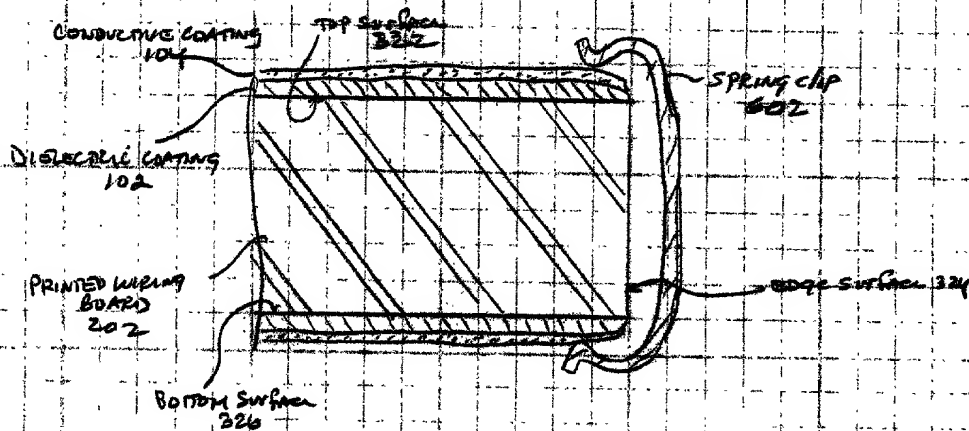


FIGURE 6E

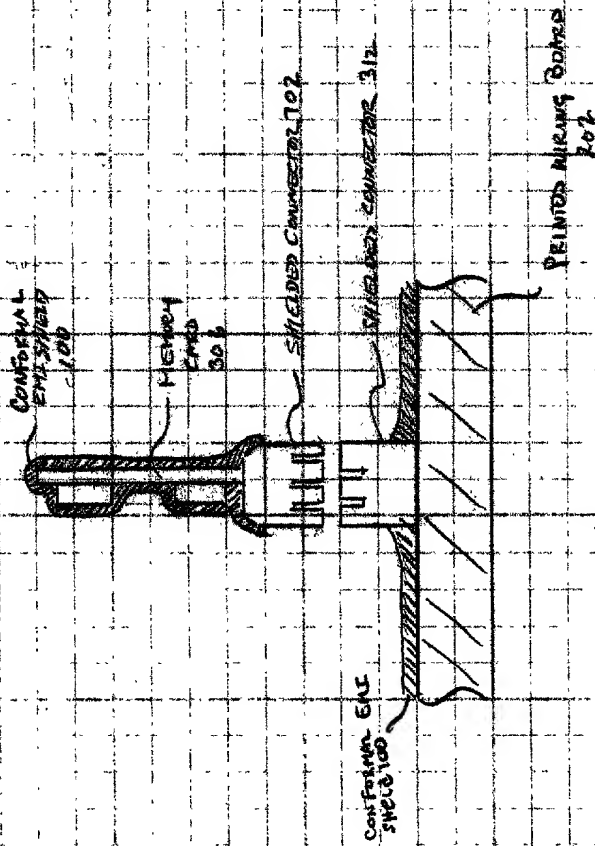


Figure 7



22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS

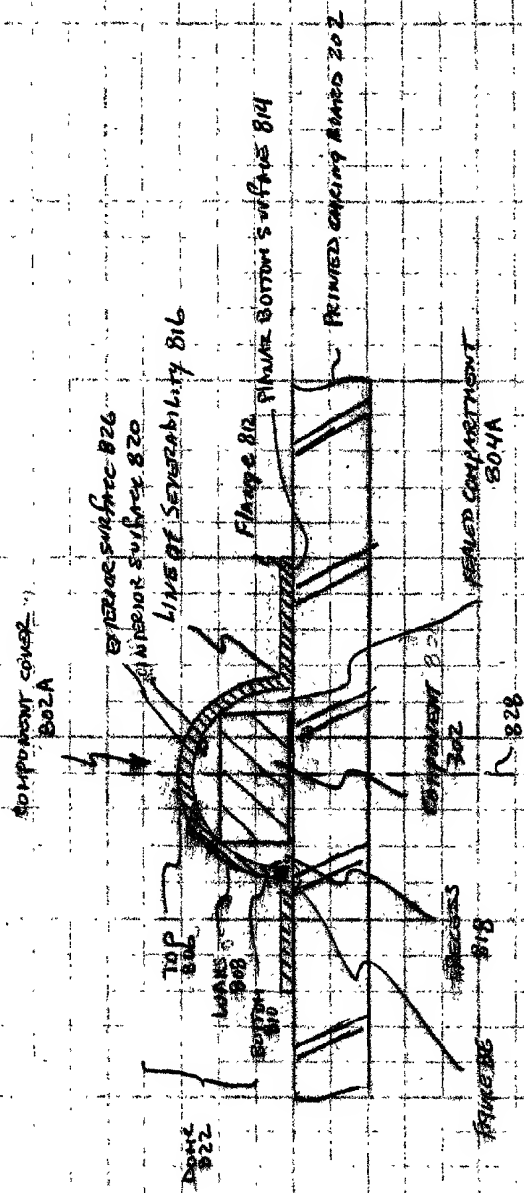


Figure 3A

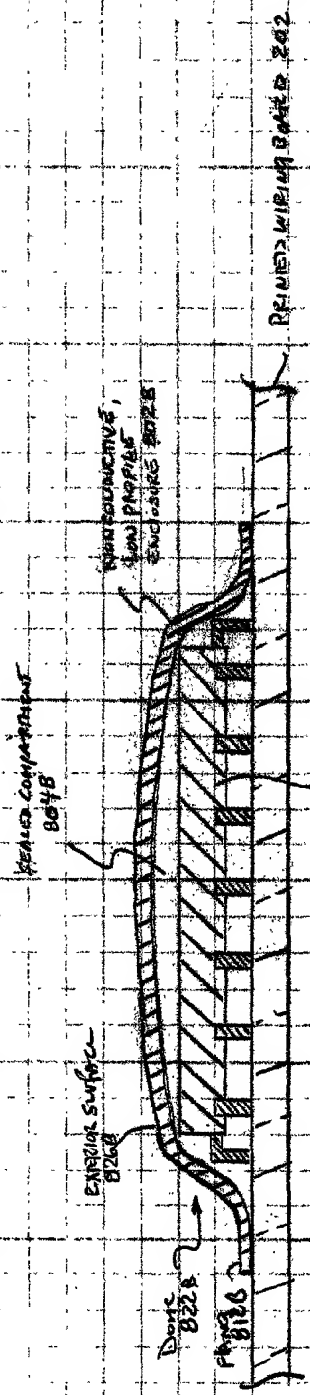


Figure 8B

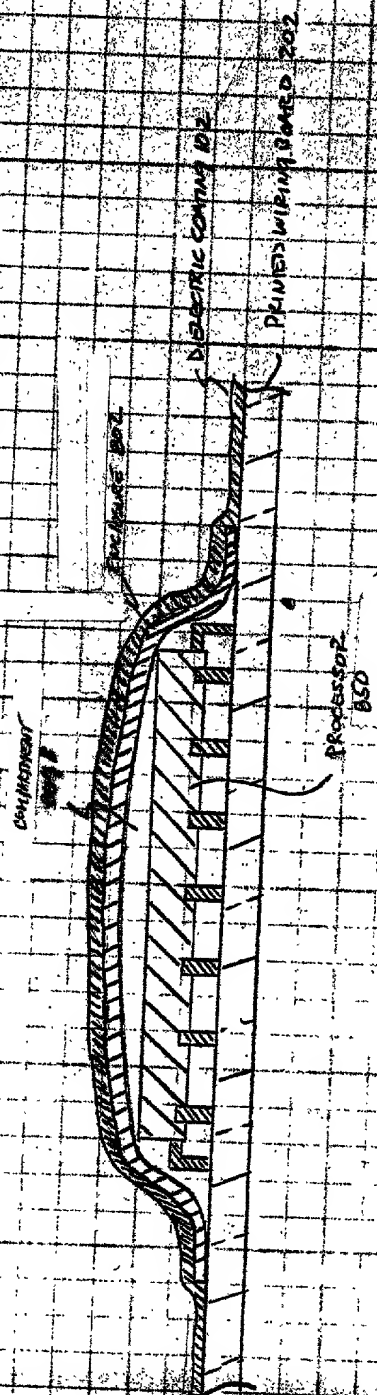


FIGURE 8C



22-141 50 SHEETS
 22-142 100 SHEETS
 22-144 200 SHEETS

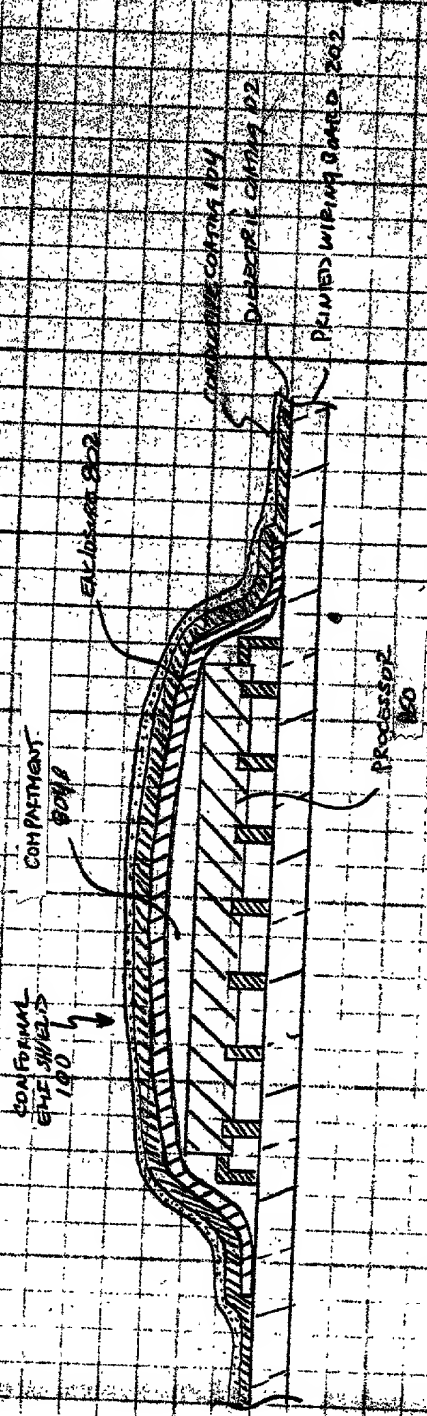


FIGURE 80



22-141 50 SHEETS
 22-142 100 SHEETS
 22-144 200 SHEETS



FIGURE 85-1

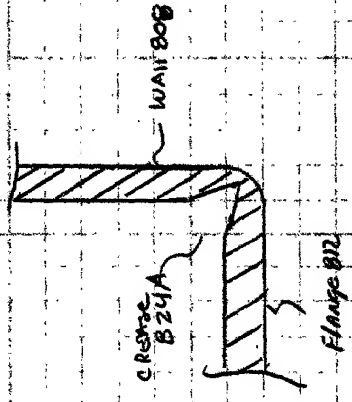


FIGURE 85-1

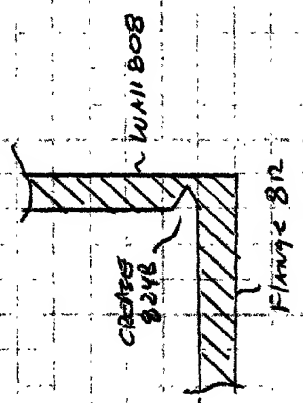
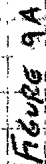
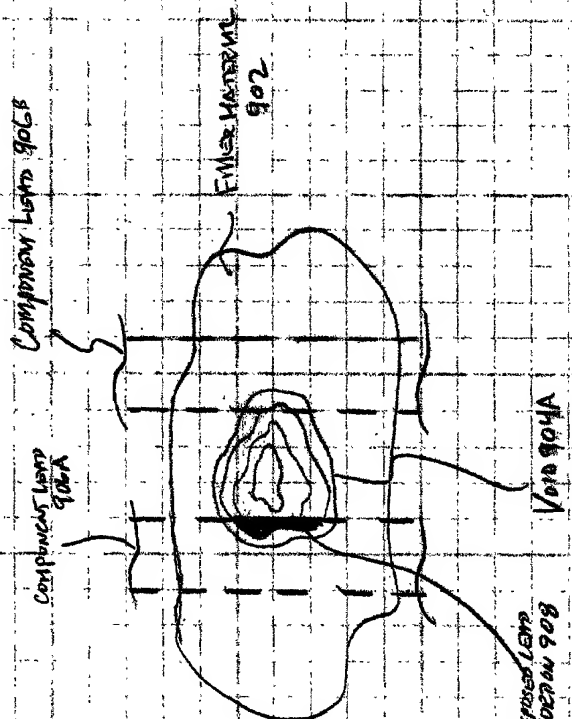


FIGURE 85-2

FIGURE 85





806 M02200 9107 900000

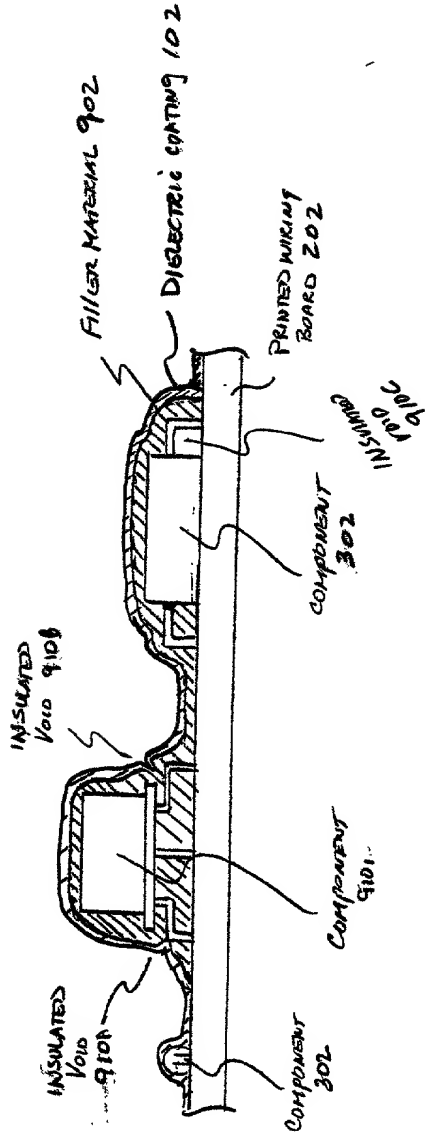


FIGURE 9c



22-141 50 SHEETS
 22-142 100 SHEETS
 22-144 200 SHEETS

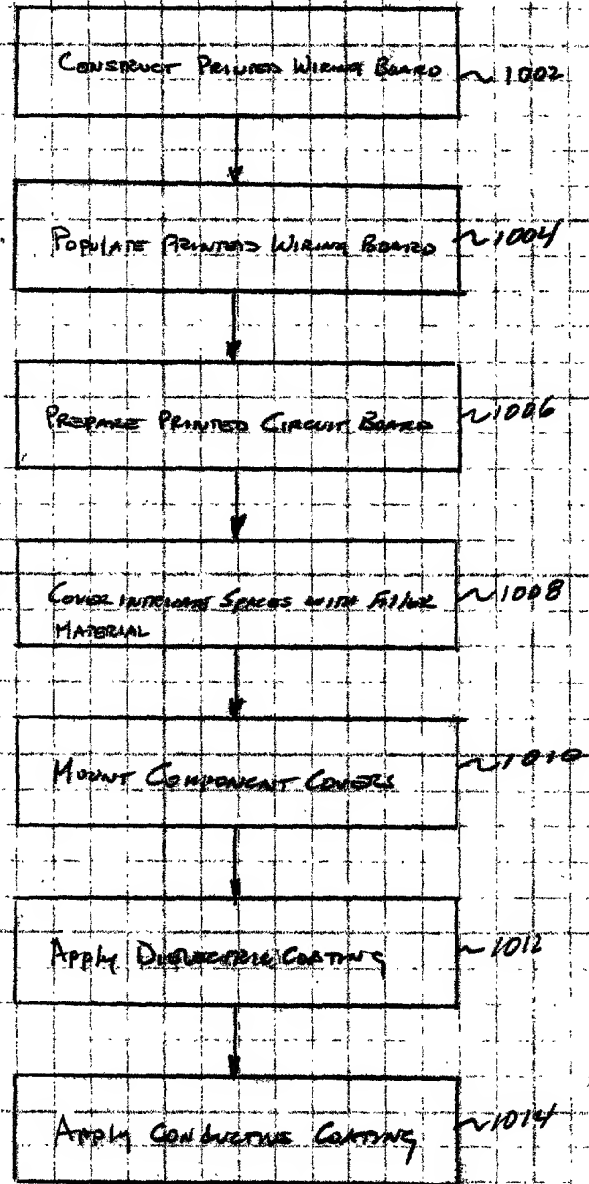


Figure 10



22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS

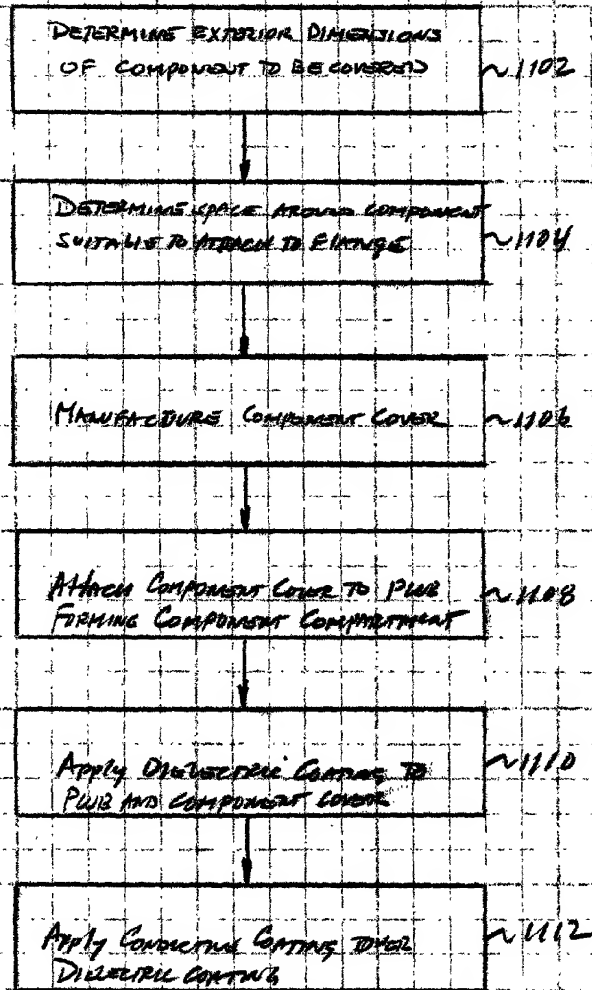


FIGURE 11